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TOWNSEND and TOWNSEND and CREW LLP

By:

Lisa Jeanetta
Lisa Jeanetta

**AMENDMENT UNDER 37 CFR 1.116
EXPEDITED PROCEDURE –
EXAMINING GROUP 2825**

PATENT
Attorney Docket No.: 015114-069200US
Client Ref. No.: A01256

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Ketan Padalia et al.

Application No.: 10/716,309

Filed: November 17, 2003

For: TECHNIQUES FOR GROUPING
CIRCUIT ELEMENTS INTO LOGIC
BLOCKS

Customer No.: 26059

Confirmation No. 6902

Examiner: Nghia M. Doan

Technology Center/Art Unit: 2825

**AMENDMENT UNDER 37 CFR 1.116
EXPEDITED PROCEDURE EXAMINING
GROUP 2825**

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Advisory Action mailed December 27, 2006, which was prompted by a Notice of Appeal filed on November 20, 2006 in response to the final Office Action mailed May 22, 2006, on the above-referenced application, please enter the following amendments and remarks along with the accompanying request for continued examination (RCE):

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.